



- S75: (9) S74 and (switch\$3 near\$5 mix\$3)
- S63: (10) S62 and (AGC or "automatic gain control")
- S52: (0) S51 and (harmonic near\$5 suppress\$3)
- S71: (10) S70 and differential
- S66: (10) S65 and switch\$3
- S56: (3) S55 and (capacitor or capacitance)
- S64: (10) S63 and "low pass filter"
- S61: (10) S60 and (load near\$3 capacitor)
- S44: (0) S28 and "phase alternating mixer"
- S67: (10) S66 and (LNA or "low noise amplifier")
- S46: (115) S45 and mixer
- S37: (7) S36 and switch\$3
- S32: (23) S31 and (capacitor or capacitance)
- S31: (32) S29 and (AGC or "automatic gain control")
- S36: (9) S35 and "low pass filter"
- S43: (0) S40 and (harmonic near\$5 suppress\$3)
- S39: (6) S37 and RF
- S72: (1463) "differential transistor pair"
- S55: (5) S54 and (wireless or mobile)
- S35: (23) S32 and (LO or "local oscillator")
- S79: (3) S77 and baseband

Favorites

United States
Patent Application Publication 455 334 A1
Pub. No. US 2004/0142674 A1
Pub. Date Jul. 22, 2004

ABSTRACT

A direct conversion receiver having a low pass pole implemented with a harmonic rejection mixer and method of operation. The receiver includes a low pass filter, a harmonic rejection mixer, and a low noise amplifier. The receiver is configured to receive a radio frequency signal and convert it to a baseband signal. The receiver includes a low pass filter, a harmonic rejection mixer, and a low noise amplifier. The receiver is configured to receive a radio frequency signal and convert it to a baseband signal.

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	U	I	Document ID	Issue Date	Pages	Title	Current OR	Current X	Ret	Inventor
3			US 20040142674 A1	20040722	12	Direct conversion receiver having a low pass pole implemented with	455/334	455/313		Kuiri, Tapio
4			US 7130604 B1	20061031	16	Harmonic rejection mixer and method of operation	455/302	455/318; 455/323;		Wong, Hee et al.
5			US 7116945 B2	20061003	61	Adaptive radio transceiver with an antenna matching circuit	455/78	333/103; 333/109;		Moloudi, Shervin et al.
6			US 7062248 B2	20060613	13	Direct conversion receiver having a low pass pole implemented with	455/334	375/297; 375/318;		Kuiri, Tapio

Hits Details HTML

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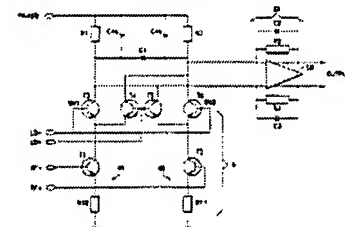
CAP NUM



- ☑ S47: (51) S46 and (AGC or "automatic gain control")
- ☑ S33: (0) S32 and (LNA or "low noise amplifier")
- ☑ S45: (261) S28 and (phase near3 alternat\$3)
- ☑ S38: (0) S37 and (wireless or mobile)
- ☑ S73: (200) S72 and (transmitter and receiver)
- ☑ S68: (0) S67 and (harmonic near5 suppress\$3)
- ☑ S49: (6) S48 and (wireless or mobile)
- ☑ S48: (18) S47 and (LNA or "low noise amplifier")
- ☑ S74: (91) S73 and (LO or "local oscillator")
- ☑ S76: (3) S75 and (LPF or "low pass filter")
- ☑ S70: (10) S69 and demultiplex\$3
- ☑ S69: (10) S67 and (LO or "local oscillator")
- ☑ S34: (1) S31 and (LNA or "low noise amplifier")
- ☑ S40: (6) S39 and load
- ☑ S53: (3) S51 and harmonic
- ☑ S58: (2591) "direct conversion" and receiver
- ☑ S59: (107) S58 and (gilbert near3 mixer)
- ☑ S60: (25) S59 and (phase near5 (alternat\$3 or adjust\$3))
- ☑ S29: (67) S28 and (PAM or "phase alternating mixer")
- ☑ S62: (10) S61 and ("SAW filter" or "surface acoustic wave")
- ☑ S51: (5) S50 and load
- ☑ S75: (9) S74 and (switch\$3 near5 mix\$3)

United States
Patent Application Publication
Pub. No. US 2004/0142571 A1
Pub. Date Feb. 26, 2004

ABSTRACT
A direct conversion receiver having a low pass pole implemented with an active low pass filter. The receiver includes a mixer, a low pass filter, and a baseband processor. The mixer is configured to receive an RF signal and a local oscillator signal and to output a baseband signal. The low pass filter is configured to filter the baseband signal and to output a filtered baseband signal. The baseband processor is configured to process the filtered baseband signal and to output a baseband output signal.



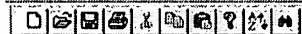
BRS form IS&R form Image Text HTML

	U	1	Document ID	Issue Date	Pages	Title	Current OR	Current X	Ret	Inventor
3	☐	☑	US 20040142674 A1	20040722	12	Direct conversion receiver having a low pass pole implemented with	455/334	455/313		Kuiri, Tapio
4	☐	☐	US 7130604 B1	20061031	16	Harmonic rejection mixer and method of operation	455/302	455/318; 455/323;		Wong, Hee et al.
5	☐	☐	US 7116945 B2	20061003	61	Adaptive radio transceiver with an antenna matching circuit	455/78	333/103; 333/109;		Moloudi, Shervin et al.
6	☐	☐	US 7062248 B2	20060613	13	Direct conversion receiver having a low pass pole implemented with	455/334	375/297; 375/318;		Kuiri, Tapio

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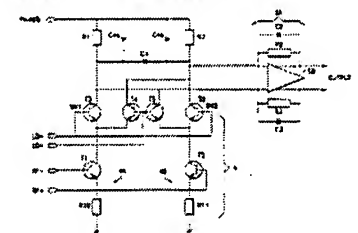
CAP NUM



- ☑ S1: (75850) "455"/\$.ccls.
- ☑ S2: (11299) S1 and (mixing or mixer)
- ☑ S3: (2520) S2 and (switching near8 signal)
- ☑ S4: (25) S3 and (harmonic near5 suppression)
- ☑ S5: (73) S3 and (harmonic near5 suppress\$3)
- ☑ S6: (46) S5 and (LPF or "low pass filter")
- ☑ S7: (16) S6 and "transistor pair"
- ☑ S8: (15) S7 and differential
- ☑ S9: (0) S8 and "capacitive load"
- ☑ S10: (15) S8 and (capacitance or capacitor)
- ☑ S11: (15) S10 and load
- ☑ S12: (14) S11 and (network near8 (capacitance or capacitor))
- ☑ S13: (0) S12 and demultiplex\$3
- ☑ S14: (14) S12 and (receiver and transmitter)
- ☑ S15: (7) S12 and (receiver and transmitter)
- ☑ S16: (51794) switching and (mixing or mixer)
- ☑ S17: (371) S16 and (harmonic near5 suppress\$3)
- ☑ S18: (133) S17 and (receiver and transmitter)
- ☑ S19: (12) S18 and "transistor pair"
- ☑ S20: (1) S19 and demultiplex\$3
- ☑ S21: (12) S19 and (capacitance or capacitor)
- ☑ S22: (12) S21 and load

United States
Patent Application Publication
Pub. No. US 2004/0142674 A1
Pub. Date: Jul. 22, 2004

ABSTRACT
A system for processing signals in a direct conversion receiver having a low pass pole implemented with an active low pass filter. The system includes a mixer and a low pass filter. The mixer is configured to receive a radio frequency signal and a local oscillator signal and to output a baseband signal. The low pass filter is configured to filter the baseband signal and to output a filtered baseband signal. The system also includes a demultiplexer and a multiplexer. The demultiplexer is configured to receive the filtered baseband signal and to output a plurality of signals. The multiplexer is configured to receive the plurality of signals and to output a single signal.



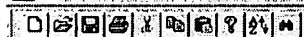
BRS form IS&R form Image Text HTML

	U	I	Document ID	Issue Date	Pages	Title	Current OR	Current X	Ret	Inventor
3	<input type="checkbox"/>	<input checked="" type="checkbox"/>	US 20040142674 A1	20040722	12	Direct conversion receiver having a low pass pole implemented with	455/334	455/313		Kuiri, Tapio
4	<input type="checkbox"/>	<input type="checkbox"/>	US 7130604 B1	20061031	16	Harmonic rejection mixer and method of operation	455/302	455/318; 455/323;		Wong, Hee et al.
5	<input type="checkbox"/>	<input type="checkbox"/>	US 7116945 B2	20061003	61	Adaptive radio transceiver with an antenna matching circuit	455/78	333/103; 333/109;		Moloudi, Shervin et al.
6	<input type="checkbox"/>	<input type="checkbox"/>	US 7062248 B2	20060613	13	Direct conversion receiver having a low pass pole implemented with	455/334	375/297; 375/318;		Kuiri, Tapio

Hits Details HTML

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CAP NUM


☐ Pending

☒ Active

- ☒ L1: (1737) "differential transistor pair"
- ☒ L2: (92122) "455"/\$.ccls.
- ☒ L3: (13518) L2 and (mixing or mixer)
- ☒ L4: (3048) L3 and (switching near\$ signal)
- ☒ L5: (107) L4 and (harmonic near\$ suppress\$3)
- ☒ L6: (67) L5 and (LPF or "low pass filter")
- ☒ L7: (27) L6 and "transistor pair"
- ☒ L8: (26) L7 and differential
- ☒ L9: (26) L7 and differential
- ☒ L10: (25) L9 and (capacitance or capacitor)
- ☒ L11: (25) L10 and load
- ☒ L12: (23) L11 and (network near\$ (capacitance or capacitor))
- ☒ L13: (14) L12 and (receiver and transmitter)
- ☒ L14: (1737) "differential transistor pair"
- ☒ L15: (273) L14 and (transmitter and receiver)
- ☒ L16: (134) L15 and (LO or "local oscillator")
- ☒ L17: (16) L16 and (switch\$3 near\$ mix\$3)
- ☒ L18: (7) L17 and (LPF or "low pass filter")
- ☒ L19: (7) L18 and load
- ☒ L20: (7) L19 and baseband

 US-PG Pub; US PAT; EPO

☒ Plurals

 Default operator:
☒ Highlight all hit terms initially

L19 and baseband

	U	1	Document ID	Issue Date	Pages	Title	Current OR	Current X	Ret	Inventor
3	<input type="checkbox"/>	<input checked="" type="checkbox"/>	US 20040142674 A1	20040722	12	Direct conversion receiver having a low pass pole implemented with	455/334	455/313		Kuiri, Tapio
4	<input type="checkbox"/>	<input type="checkbox"/>	US 7130604 B1	20061031	16	Harmonic rejection mixer and method of operation	455/302	455/318; 455/323;		Wong, Hee et al.
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